

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(remaining battery power) and (level shifter circuit) and (N channel MOS transistor)	US-PGPUB	WITH	OFF	2006/03/30 15:59
L2	0	(remaining battery power) and (level shifter circuit) and (N channel MOS transistor)".clm"	US-PGPUB	WITH	OFF	2006/03/30 15:59